

I/O CONNECTIONS
 BURCHED PLUGON MODULE COMPATIBLE
 ALSO HP LOGIC ANALYSER CONNECTOR COMPATIBLE

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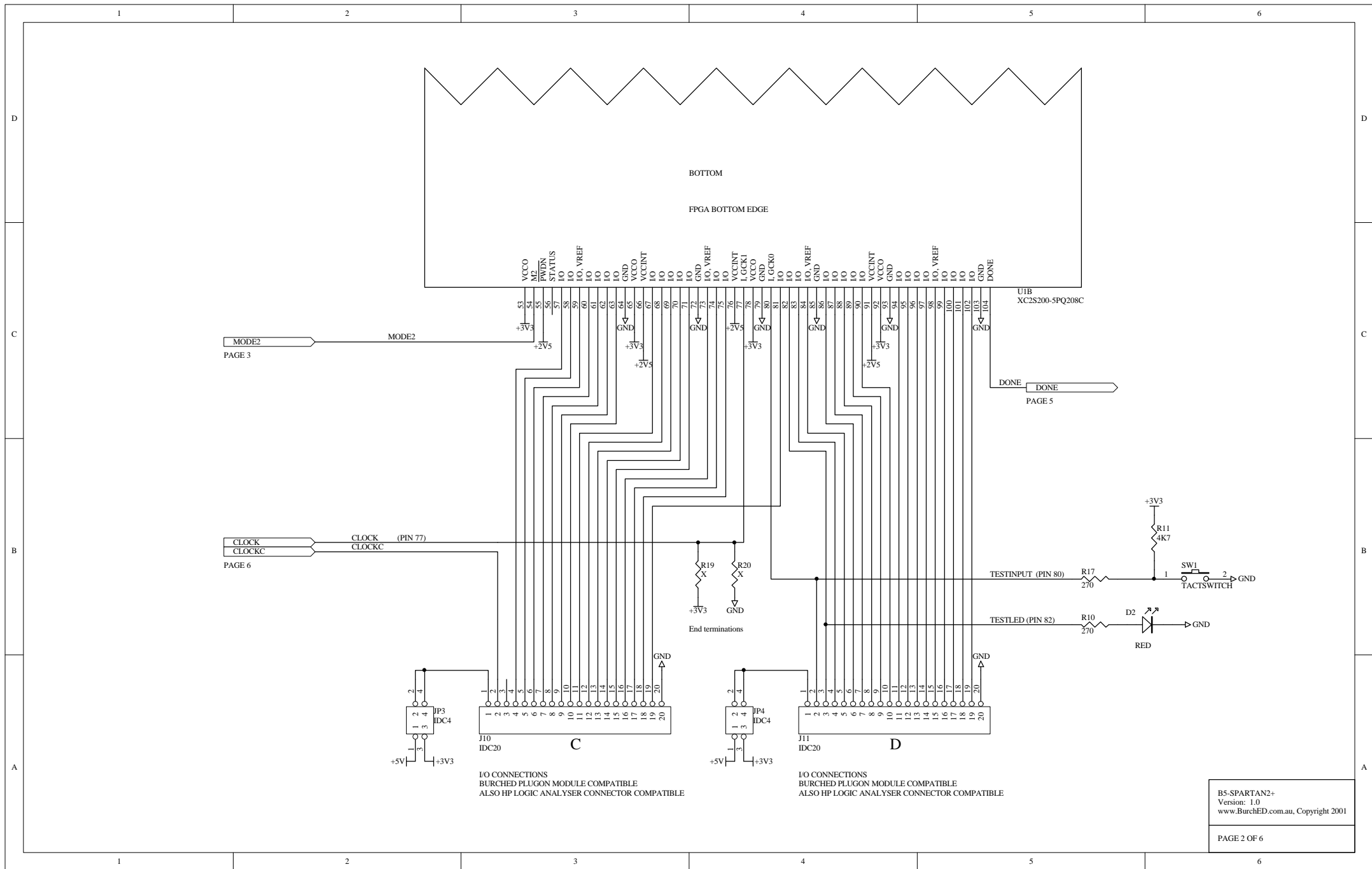
Side Header Shunts:

- Remove the header shunts if you are going to:
 (1) Directly connect a logic analyser cable to a 20-pin header, or
 (2) Connect 20-pin headers on two separate kits (eg. via a flatcable)
 (avoids contention of the power supplies)

Ensure that the side header shunts are inserted
 (select either 3.3V or 5V)
 when using the BurchED Plug-On Modules

JTAG MODE DOWNLOAD

PAGE 3



D

D

C

C

B

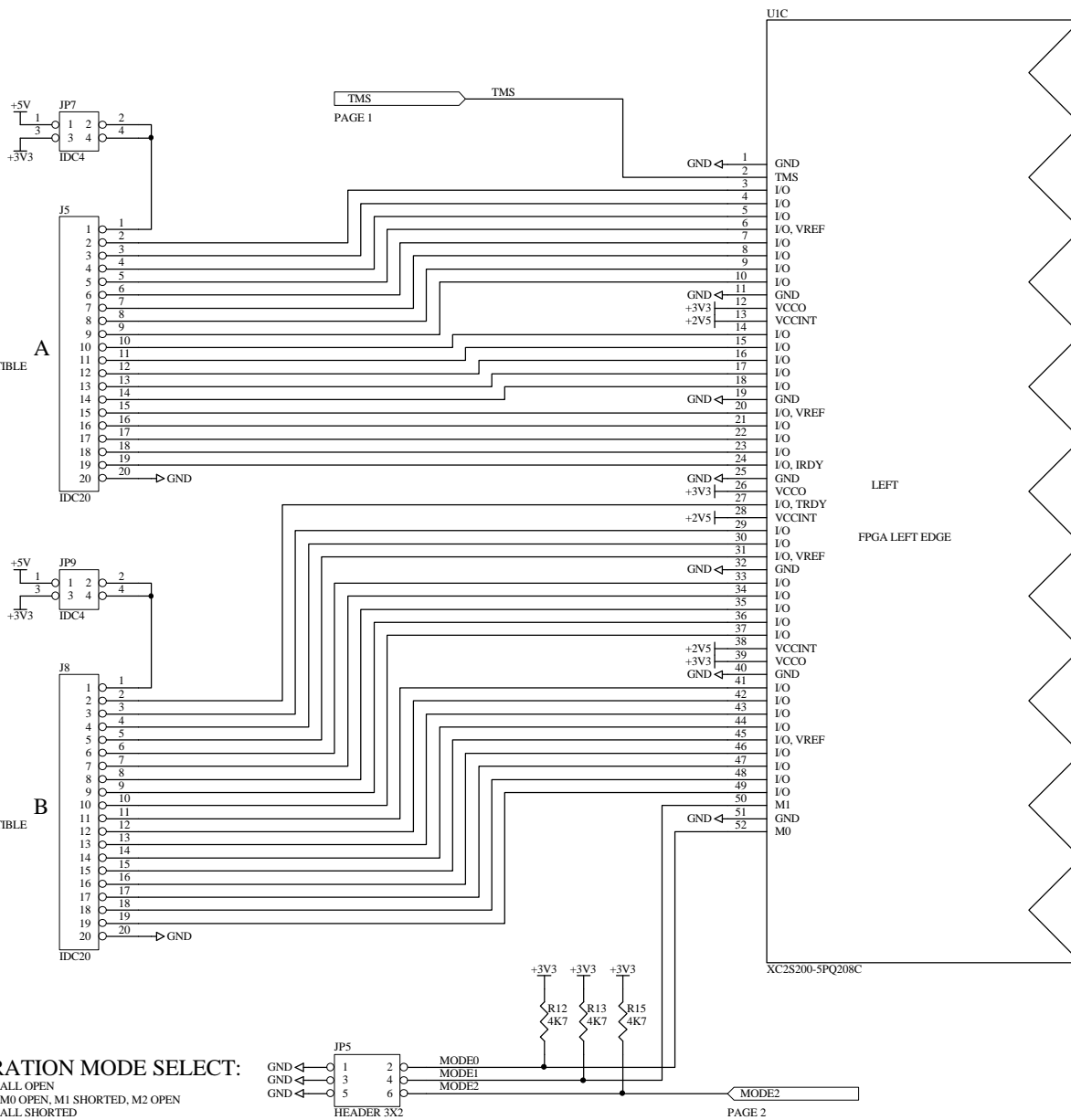
B

A

A

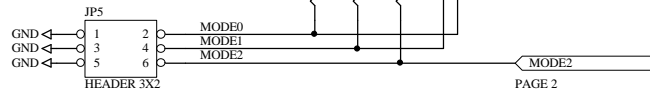
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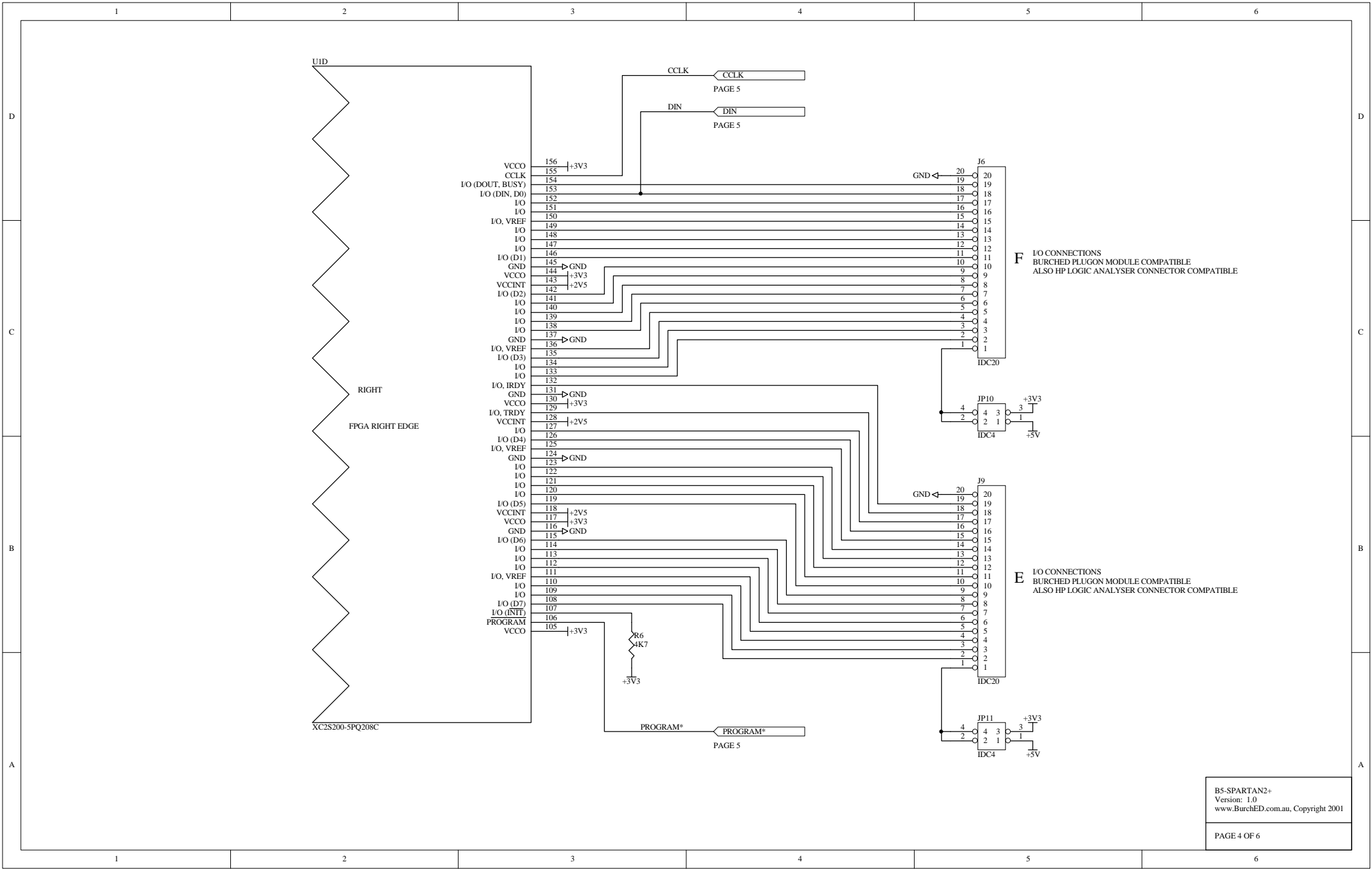
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CONFIGURATION MODE SELECT:

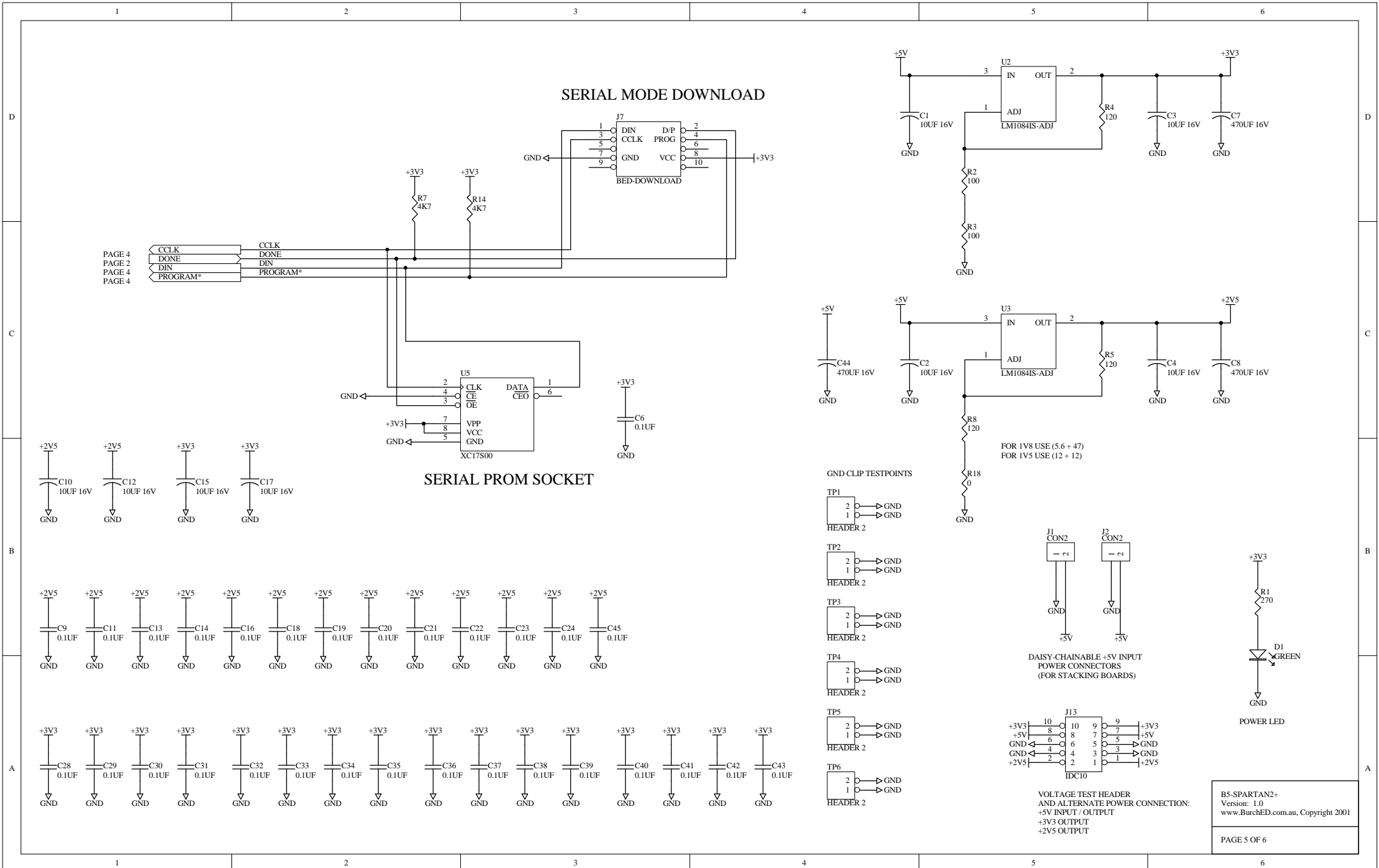
SERIAL MODE : ALL OPEN
JTAG MODE : M0 OPEN, M1 SHORTED, M2 OPEN
SERIAL PROM : ALL SHORTED



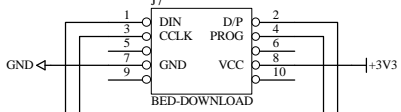


F I/O CONNECTIONS
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E I/O CONNECTIONS
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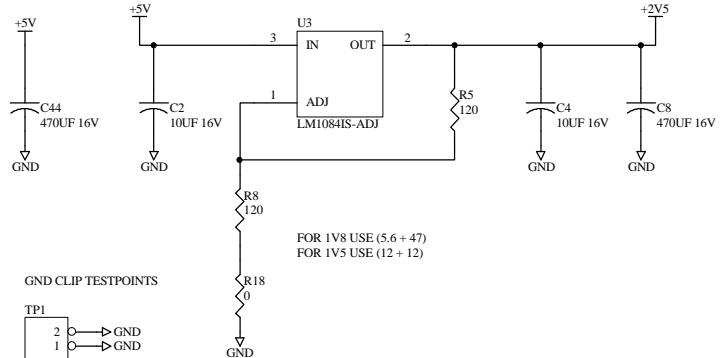
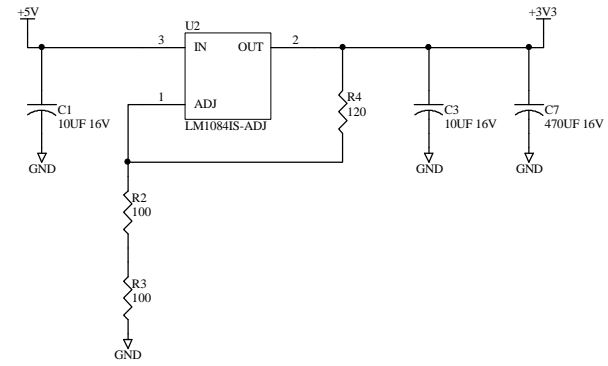
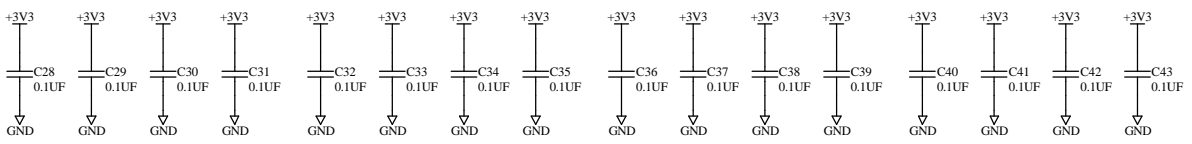
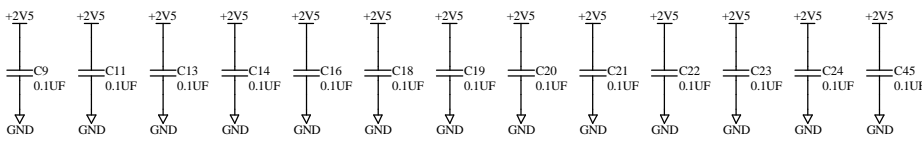
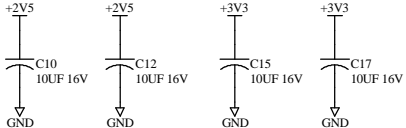
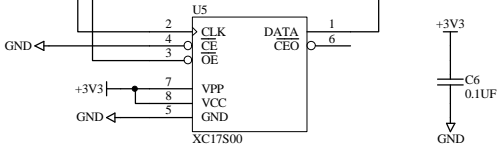


SERIAL MODE DOWNLOAD



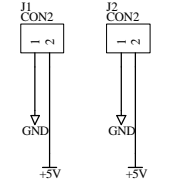
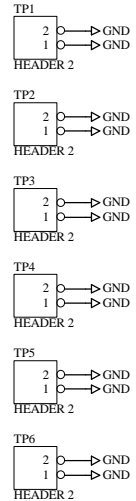
PAGE 4 CCLK
 PAGE 2 DONE
 PAGE 4 DIN
 PAGE 4 PROGRAM*

SERIAL PROM SOCKET

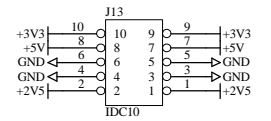


FOR 1V8 USE (5.6 + 47)
 FOR 1V5 USE (12 + 12)

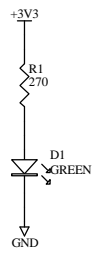
GND CLIP TESTPOINTS

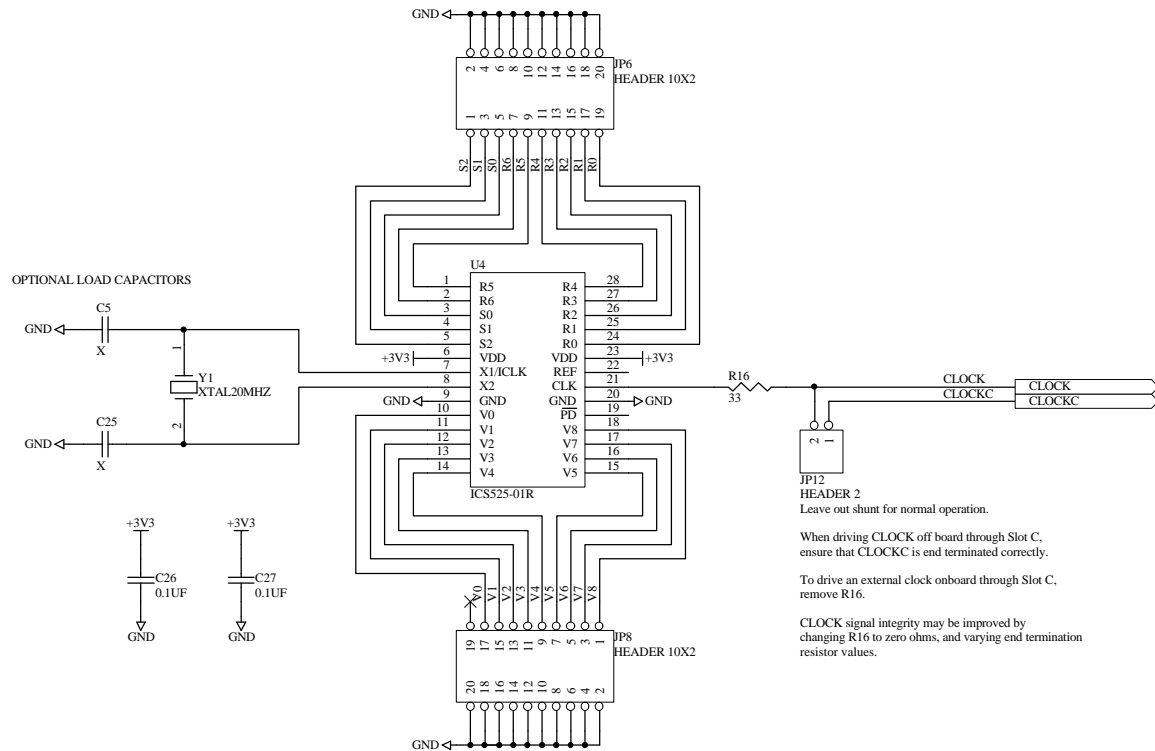


DAISY-CHAINABLE +5V INPUT
 POWER CONNECTORS
 (FOR STACKING BOARDS)



VOLTAGE TEST HEADER
 AND ALTERNATE POWER CONNECTION:
 +5V INPUT / OUTPUT
 +3V3 OUTPUT
 +2V5 OUTPUT





JP12
HEADER 2
Leave out shunt for normal operation.

When driving CLOCK off board through Slot C, ensure that CLOCK is end terminated correctly.

To drive an external clock onboard through Slot C, remove R16.

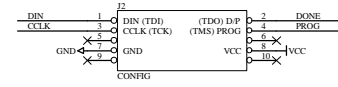
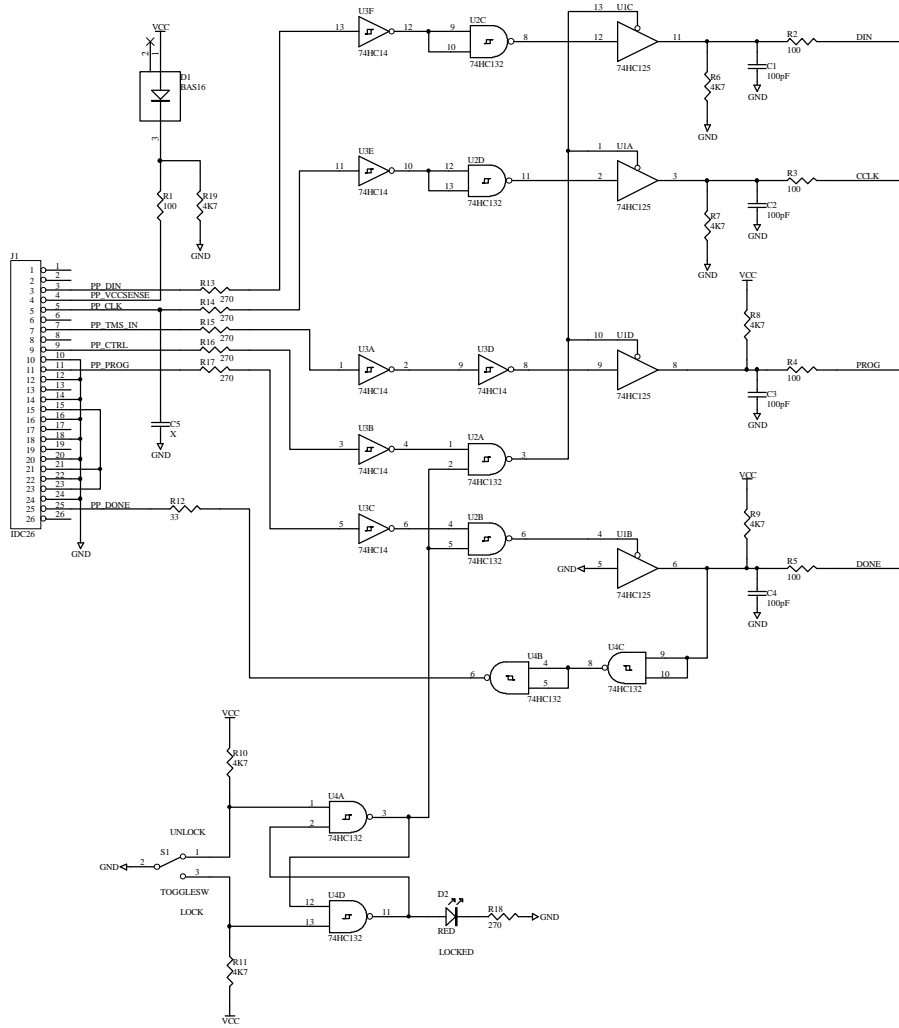
CLOCK signal integrity may be improved by changing R16 to zero ohms, and varying end termination resistor values.

PROGRAMMABLE PLL OSCILLATOR GENERATES ANY FREQUENCY BETWEEN 1MHZ AND 100MHZ

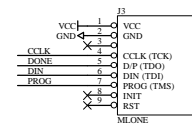
USE THE ONLINE ICS525-01 CALCULATOR AT:
www.icst.com/products/ics525inputForm.html
 (for the calculator use an input of 20MHz and supply of 3.3V)

EXAMPLE PLL OSCILLATOR SETTINGS:
 (0 = SHORTED, 1 = OPEN)

	1MHz	3.6864MHz (-34 ppm)	4MHz	20MHz	24MHz	25.175 MHz (+17.4 ppm)	48MHz	66MHz	80MHz	100MHz
S2	0	0	0	0	1	1	0	0	0	0
S1	0	0	0	1	0	0	0	0	0	0
S0	0	0	0	0	0	1	1	1	1	1
R6	0	0	0	0	0	1	0	0	0	0
R5	1	1	0	0	0	0	0	0	0	0
R4	0	1	0	0	0	0	0	0	0	0
R3	1	0	1	0	0	1	0	1	0	0
R2	1	0	0	0	0	0	0	0	0	0
R1	1	0	1	0	1	1	1	0	0	0
R0	0	1	0	1	0	0	1	0	1	1
V8	0	0	0	0	0	1	0	0	0	0
V7	0	0	0	0	0	0	0	0	0	0
V6	0	0	0	0	0	0	0	0	0	0
V5	0	1	0	0	0	0	0	0	0	0
V4	0	0	0	0	0	1	0	1	0	0
V3	0	0	0	0	0	0	0	0	0	0
V2	1	1	1	1	1	1	1	0	1	1
V1	0	1	0	0	0	1	0	0	0	1
V0	0	1	0	0	0	1	0	1	0	1



CONFIGURATION CONNECTOR
SERIAL MODE AND JTAG MODE DOWNLOAD



SINGLE-IN-LINE CONFIGURATION CONNECTOR
SERIAL MODE AND JTAG MODE DOWNLOAD
MULTI-LINX FLYING LEAD CONNECTOR SET #1 COMPATIBLE

